

Curriculum Vitae of **Claudio Urso**



Born at Zollino (Lecce), Italy on February the 4th, 1976, Italian.

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EMPLOYMENT HISTORY

From 2005 to
now

EXPRIVIA TELCO & MEDIA (formerly Devoteam auSystems), Milan, Italy.

Consulting Engineer: professional technical consultancy services to the customers in electronics and communications engineering. In the last years I have been working for important customers such as Cisco Systems, ADB Broadband (formerly Pirelli Broadband Solutions), Sky Italia and Siemens each time as R&D engineer mainly aimed to the HW and SW testing activities. Follow the details of the experiences.

From 2015 to
2017

CISCO SYSTEMS, Milan, Italy.

Test Development Engineer dealing with the physical layer parametric testing of pluggable transceivers and cables within Cisco TMG (Transceiver Module Group).

The activity concerns:

- the release of pluggable transceivers and cables for Data Center, Access, Metro, Carrier Ethernet, Service providers and Storage networking applications across Cisco switching and routing platforms;
 - vendor qualification and selection for new product introduction;
 - vendors' coordination aimed to the products specification and the prototypes management.
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From 2014 to
2015

CISCO SYSTEMS, Milan, Italy.

System Test Engineer dealing with the SW validation of SONET/SDH, DWDM systems. It includes system test definition, test plan definition, scheduling and execution, test bed installation and maintenance. Customer oriented test plan activity, such as network installation, upgrade, maintenance, troubleshooting, usability and documentation. Interaction and cooperation with development teams for product definition and functional specifications.

In particular, the activities were the following:

- analysis of the System Functional specifications and any new product feature. Cisco design specifications and documents study, DWDM hardware platform and test plan preparation, design and check of the test cases to verify features are compliant with the specifications.
 - execution of the required tests in Cisco lab, using Cisco tools to manage DWDM nodes, to verify all new features working properly, failures discovering, test cases results reporting.
 - failures managing: anomalies and defects discovered during test phases detailed reporting and explaining.
 - support to developers giving valid help to development team in troubleshooting and resolving failures by performing again the failed tests to verify the proposed solutions.
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EMPLOYMENT HISTORY

From 2011 to
2014

ADB BROADBAND (formerly Pirelli Broadband Solutions), Milan, Italy.

Testing Engineer dealing with the development and integration of IEEE 802.11a/b/g/n Wi-Fi technologies for ADB residential/business gateways. The activities included:

- test analysis and planning for performance validation and optimization;
- prescan activities for CE certification (according to ETSI/IEEE test plans) and devices interoperability (according to Wi-Fi Alliance and 3GPP test plans);
- antenna design and integration;
- integration of GSM/UMTS/LTE and DECT for residential and business gateways.
- new solutions analysis for IEEE 802.11ac.

The activities included build test environments and maintaining lab network infrastructure; prepare test plans, test environments and direct manual testing strategies; conduct benchmark performance using various instrumentation; developing of automatic test benches, troubleshooting applied to products and processes and study of new solutions and improvements.

Activities include also external power supply and power consumption; Ethernet and USB Physical Layer Test; USB Current Driving; Long Term Performance analysis; Prototype Thermal Mapping.

From 2009 to
2011

PIRELLI BROADBAND SOLUTIONS, Milan, Italy.

Design Validation Test Engineer dealing with the testing of Home Access Gateway and Set Top Box products as follow:

- Home Access Gateways: HW validation of different broadband access devices designed for world-wide Telco Operator requirements. HW and SW testing of ADSL/ADSL2/2+ technology (ITU G992.X) products such as the analysis of performance and interoperability for systems consisting of DSLAMs and CPE modems (TR-067 and TR-100).
 - Set Top Boxes: HW and SW validation of Pirelli Broadband Solutions decoders against DTV international standards (ETSI DVB-T, ETSI DVB-S/S2, NorDig, IPTV...). It includes Digital and Analog TV testing: RF quality measurements (Error Performance Analysis); Analog Video Analysis (CVBS, RGB, Component, S-Video...); HDMI Video Analysis (Protocol, EDID, HDCP...); HDMI Electrical Compliance; Digital and Analog Audio Validation (MPEG-1 L2, Dolby Digital 5.1 and AC3); Video streaming (MPEG-2 and AVC H264) analysis.
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From 2008 to
2009

SKY ITALIA, Milan, Italy.

STB Test Engineer dealing with the validation of all SKY Set top Box platforms (SD, HD, PVR SD and PVR HD) against internal and international standards (DVB-S/S2 / ETSI).

It included both hardware and software STB testing:

- RF Tuner measurements; Digital satellite environment setting and testing (LNB, DiseqC, SMATV, SCR/SDU, SCR/MDUFSK);
 - DVB (MPEG-2) analysis (Program Specific Information and Service Information); Video, Still and OSD display level analysis; MPEG-1 (Layers 1 and 2) and Dolby Digital Audio Drivers analysis.
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EMPLOYMENT HISTORY

From 2006 to
2008

CISCO SYSTEMS, Milan, Italy.

HW Product Validation Engineer dealing with the validation of Optical Network Systems in SDH/Sonet technologies as follows:

- Hardware Quality Assurance. Physical Layer validation against International Standard and International Specification; Optical Physical Layer testing of transmission cards, optical amplifier and optical filters; Electrical Physical Layer of electrical cards. Measurements and Analysis over stated operating parameters and under extreme environmental.
 - Electronic Design Validation Test. The EDVT test is designed to ensure that the Device Under Test has achieved an acceptable level of: Design stability; Sustained performance over extreme environmental condition; Robustness to parametric variances of Frequency and Voltage margining; "Readiness" for manufacturing.
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From 2005 to
2006

SIEMENS, Milan, Italy.

Integration Test Engineer dealing with the Validation of Microwave Radio Transmission Systems. In particular the Analysis of the ODU (Out Door Unit) of Digital PP Systems in both PDH and SDH technologies as follow:

- Troubleshooting on 'return from the field' (customer) and from the factory to individualize critical aspect of project and systematic problems of production (with investigations both to system level and to circuit level) to compile statistics of typology and modality of breakdown;
 - Validation of new equipment with measures for R&TTE directive documentation;
 - RF measurements such as Frequency Error/Stability, Transmitted Power, Tx Spurious Emissions, BER analysis, analysis of Transmission Spectrum Mask quality, ATPC Operation, Co-channel/Adjacent-channel interference sensitivity, Noise Figure;
 - Baseband measurements: AIS Frequency Stability, Bit-Rate, Output Pulse Shape; Jitter.
 - ETSI thermal Cycles and verification coherence of the alarmistic.
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From 2004 to
2005

AGM INDUSTRIE, Lecce, Italy.

Research Engineer dealing with the design of an integrated system for measuring and monitoring liquid and granular substances by using the Time Domain Reflectometry (TDR) technique.

From 2004 to
2005

UNIVERSITY OF LECCE, Lecce, Italy.

Research Engineer. Research field focalized on a Time Domain Reflectometry (TDR) based technique for following main applications: environmental monitoring (water, soil, structural deformations); level control of rivers, lakes, wells, dams, etc.; monitoring of tanks, vessels, silos, etc.; transmission line control and fault detection; industrial process control involving liquids or granular substances.

From 2002 to
2003

NLR (NATIONAL AEROSPACE LABORATORY), Marknesse, The Netherlands.

Antenna Modeling Trainee. Computational electromagnetic modelling of stacked patch antennas by mean of a hybrid full-wave method.

EDUCATION

- 1996-2003 **Master Degree in Telecommunications Engineering**
Department of Information Engineering (D.I.I.), University of Siena, Italy.
- Final thesis in Electromagnetics:** the work concerned with hybrid full-wave methods based on Finite Element Method and Method of Moments. Supervisors: Professor Stefano Maci and Doctor Harmen Schippers of National Aerospace Laboratory (NLR), The Netherlands. Full mark: 100/110.
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- 2004-2005 **Ph.D in Information Engineering.**
Department of Innovation Engineering (D.I.I.), University of Lecce, Italy.
- First year passed on 12th July 2005.
Subject: microwaves techniques and open-ended antennas for monitoring and control purposes. Main activity concerns with Time Domain Reflectometry (TDR) technique.
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- 2003 **Professional Practice Examination**
University of Florence, Italy. Examination for the qualification to practice the profession of Engineer.
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- 2000-2001 **ERASMUS Programme**
Technical University of Denmark (DTU), Lyngby, Denmark.
- Special course of Measurement of Antennas (Prof. Olav Breinbjerg).
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- 2005 **Ph.D Course on "Phased Array and Reflectarrays"** in the frame of European School of Antennas Council of Excellences (ACE), TNO, Den Hagg, The Netherlands.
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- 2008-2009 **English Course**
8months training course (once a week) for English comprehension and improvement at Wall Street Institute, Milan, Italy. Upper Waystage 1 level successfully completed.
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- 1990-1995 **High School Degree, Diploma in Technical Maturity (Informatics)**
I.T.I.S. "Enrico Fermi" Institute, Lecce, Italy
- Major subjects: Informatics, Electronics, Mathematics, Chemistry, Physics, Telecommunication systems and Communication Networks. Full Mark: 58/60.
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EXPERTISE

Language skills Mother tongue: Italian.
Very good knowledge of spoken and written English. Basic of French and basic understanding of Spanish.

Computer skills *Lab facilities and instrumentations:*

- *Optical testing:* Digital Oscilloscope TEKTRONIX DSA8300; Agilent Infiniium DCA, Anritzu Optical Spectrum Analyzer; Waveform Generator TEKTRONIX; IXIA (IP Traffic Generator); ANRITSU MD1230B (Data Quality Analyzer); Power Meter YOKOGAWA; Agilent N2X Traffic Generator; Hydra PGM117 Pulse Pattern Generator; Spirent Test Center; Smart Bits; Thermal Chambers
- *Broadband e WiFi:* ROHDE&SCHWARZ SFL Spectrum Analyzer; Agilent Infiniium DSO 90604° Digital Storage Oscilloscope; NETCOM Smartbits 200 Performance Analysis System; Agilent 8960 Series 10 Wireless Communication Test Set; E5515C/E (8960) Wireless Communications Test Set; IXIA Optixia XM2 IP Performance Tester; Xena Networks C1-M6SFP Gigabit Ethernet Test Unit; Agilent MXA N9020A Signal Analyzer; Aeroflex IFR3416 Signal Generator; ANRITZU MD1230B Data Quality Analyzer. Spirent Noise Generator DLS 5880/5500; Spirent Noise Injection DLS 5405/5410DC; Spirent Loop Emulator DSL 8234/8235/414E.
- *Analogue and Digital television:* ROHDE&SCHWARZ SFU (DVB-T Modulator, Noise and Fading Generator); ROHDE&SCHWARZ SFE (DVB-T Modulator); ROHDE&SCHWARZ ETL TV Analyzer; DVT Test System (MPEG-2 Transport Stream Generator); ANEVIA VOD Server (Video on Demand Server); Kasenna VOD Server; VM700 TEKTRONIX (Audio/Video Instrument Test); Minerva Media Gateway (DVB to gateways / streamers)

Application Tools.

CDETS (Cisco's Defect and Enhancement Tracking System); HP Quality Centre;
EDCS (Electronic Document Control System); PRRQ (Peer Review Request Queue).

Programming languages: Fortran 77/90, VB6, C, Pascal. Basic of C++.

Operating systems: Dos, Windows 9x, 2000, XP, NT, W7 and W8; Unix and Linux.

MSoftware: good knowledge of Word, Excel, PowerPoint.

Commercial Software: MATLAB and SIMULINK, LabVIEW, TestStand, knowledge of FEKO and HFSS.

Networking: LAN, TCP/IP, http, ftp, Internet.

Others programs: Corel Draw, Tecplot, Wireshark.

OTHER

Interest Music and cinema; Sports (volleyball and soccer); Cooking (and Eating); Reading; Travelling and other cultures.

Publications ON CONFERENCE PROCEEDINGS

[1] A. Cataldo, F. Attivissimo, A. Trotta, L. Tarricone, C. Urso, "Time Domain Reflectometry Technique for Monitoring of Liquid Characteristics", 2005 IEEE IMTC - Instrumentation and Measurement Technology Conference, OTTAWA, ONTARIO, CANADA 17-19 May 2005.

[2] A. Cataldo, L. Tarricone, C. Urso, "A TDR Method for efficient real-time measurement of levels and dielectric properties of liquids", Proceedings of the EuMA (European Microwave Association), N.2, Vol.1 -2005. Special Issue on Industrial Applications of Microwave Sensor Systems.

[3] A. Cataldo, F. Attivissimo, A. Trotta, L. Tarricone, C. Urso, "Qualitative and Quantitative Characterization of Liquids from TDR Measurements", 14th IMEKO Symposium on New Technologies in Measurement and Instrumentation – 10th Workshop on ADC Modelling and Testing, GDYNIA/JURATA, POLAND 12-15 September 2005.

References Available upon request.

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I authorize the collection and treatment of my personal data according to the Italian law 675/96 and the art. 13 of the D.Lgs 196/2003 and to the equivalent law in force in the countries where I apply.